

Resistance switching in transparent magnetic MgO films

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ABSTRACT

We have studied the abrupt and hysteretic changes of resistance in MgO-based capacitor devices. The switching behavior is discussed in terms of the formation and rupture of conduction filaments due to the migration of structural defects in the electric field, together with the redox events which affects the mobile carriers. The results presented in this paper suggest that MgO transparent films combining ferromagnetism and multilevel switching characteristics might pave the way for a new method for spintronic multibit data storage.

1. Introduction

The current-induced resistance switching in magnetic tunnel junctions has received a lot of attention due to its potential application in a simplified design for high density non-volatile memories. Various physical effects have been discussed in this regard to explain the induced resistance switching. Spin torque, for instance, could be of a particular interest for the development of multilevel memory cells through the vertical-current-induced domain-wall motion demonstrated both in an amorphous Al–O barrier and MgO crystalline junctions [1,2].

There is another class of devices, usually made in a two terminal metal/insulator/metal structure, which shows voltage-controlled resistance switching [3,4]. Among the various insulators that have been reported thus far, transition metal (sub) oxides such as CuO, NiO, TaO₂, TiO₂, NbO₂ and HfO₂ have attracted the most attention. Different switching modes have been proposed, including localized traps at the metal/oxide interface [5], ion-migration effects coupled to redox processes [6], and the formation of metallic nanoscopic filaments between the two electrodes [7], although the exact process is still unclear. However, there is a general consensus in the literature that the conducting channels are selectively formed at crystalline defects such as grain boundaries and vacancies [8-10]. It is noteworthy that structural defects such as cation vacancies (or first-row element impurities such as C and N) can also become prime candidates for sources of ferromagnetism in wide-gap oxides, exemplified by CaO, ZnO, and HfO₂ [11]. Therefore, a material combining reversible resistive transitions and robust ferromagnetism is expected to provide a new approach to spintronic memristive devices [12]. MgO, which holds importance in many industrial applications, is very attractive due to its matured process technology. On the one hand, MgO addition to ZnO layers (Mg_xZn_{1-x}O) has shown to improve data retention characteristics in non-volatile memory architectures [10]. Moreover, electrical switching that has been observed in MgO films [13] and MgO-based magnetic tunnel junctions [14-

16] is attributed either to nanostructural rearrangements of metallic ions in the electrode/barrier interfaces or reversible displacement of oxygen vacancies. On the other hand, defect-induced ferromagnetism in MgO has already been observed at room temperature [17-21]. Our previous results demonstrate that the presence of cation vacancies has a strong impact on the electronic properties of MgO. The directionality of the oxygen p orbitals produces a ferromagnetic state and probably influences its chemical reactivity. We expect the holes that are feebly localized on Mg vacancies could be aligned by an electric field, and thus can modify the magnetic coupling and conductivity. The purpose of this paper is to address evidences for resistive switching in magnetic MgO thin films with lattice defects situated at the cation sites.

2. Device preparation and characterization

Heterostructures were deposited by RF sputtering on Nb doped (0.5 wt%) SrTiO₃ (001) single crystal substrate (Crystec). An 80 nm MgO layer was first deposited at 450°C in an Ar–O₂ mixture. This temperature is close to the optimal annealing temperatures in usual MgO-based tunnel junctions [22]. Growth conditions have been previously observed to control the density of oxygen vacancies within the MgO [23]. In particular, adding O₂ to the Ar plasma during MgO growth also helps in altering the oxygen population and the barrier performance [24]. It is also worth mentioning that these MgO films show a remarkable structural distortion [25], which is taken as an indication of the existence of cation vacancies in our samples [17]. We surmise Mg re-evaporates from the substrate faster than it reacts with O to form MgO on the substrate so that cation vacancies are stabilized, thus resulting in a transparent magnetic film (the transmittance is above 80% in the visible spectrum with saturation magnetization values about 10 emu/cm³) as reported elsewhere [17,18,20]. The sample was then capped with a 100 nm thick transparent conductive indium–tin-oxide (ITO) layer deposited at room temperature through a shadow mask, defining an area of 1 mm in diameter.

The deposition was carried out using an ITO (In_2O_3 doped with 10 wt% SnO_2) target with 1 mTorr Ar pressure [26]. The crystallinity and the relative high electrical resistivity of those as-deposited ITO films were improved after vacuum annealing at temperatures above 350 °C for 1 h. However, it should be noted that, the device heterostructure design has, by no means, been optimized for superior performance. Electrical characterization was performed by an Agilent B1500A parameter analyzer in DC sweep mode. All the measurements were performed by the two probe method at room temperature. Measurement convention was to apply the electric potential to the top electrode while the bottom electrode was grounded.

3. Results and discussion

The all-oxide STO-n/MgO/ITO-n heterostructure is charming since the oxygen permeability of the electrodes has been demonstrated to affect the resistive switching significantly [27]. At the same time, the asymmetry of the two interfaces should define a stable switching polarity [4]. All these reasons suggest a different result pattern compared to that of Huang et al. [13].

A highly rectifying I–V curve (i.e., current can pass in one direction but not in the opposite direction) was observed in the virgin state (Fig. 1). In addition, the I–V curves exhibit a hysteretic behavior similar to the one previously reported in MgO-based magnetic tunnel junctions [15,16]. The results indicate that the charge carriers in the MgO are positive, and hence they must be holes [28]. Both the rectifying direction and subsequent switching polarity support a Schottky-like MgO/ITO contact and a more electrically conductive bottom STO/MgO interface [29]. The electroforming process by applying a high electric field across the junction (Fig. 1(b)) creates conductance channels of low resistance which are believed to be the sites for the subsequent switching. It is also worth mentioning that visible electroluminescence (photograph in Fig. 1) develops simultaneously with the

resistance switching [3,30]. Although not properly understood at this time, the close correlation between this luminescence and the memristor behavior probably indicates that both have the very same origin, i.e., the hole-doping induced by the cation vacancies. In this regard, we have shown recently that luminescence emerges at 2.6 eV in similar defective MgO films [20]. Obviously further work is required before firm conclusions can be drawn.

Repeatable switching cycles were observed after a forming step. The result from Fig. 2 is that the low resistance state changed to an insulating state at a critical voltage (points 3–4 within the figure). When the voltage was swept again, the device changed back to a conducting state. The occurrence of such a bipolar resistive switching within the MgO layer was confirmed by performing the I–V measurement for the Nb–STO substrate or ITO electrode alone, where no reversible switching was observed [31]. Furthermore, the I–V curves are quite symmetric for both polarities, which suggest that the current is limited by the bulk active layer, and not by the electrodes. The electric field, given by the voltage divided by the active layer thickness, amounts about 10^6 V/cm which compares well with the values reported in the literature [32]. The voltage for the forming step should depend significantly on the oxide layer thickness, which indicates that significant margin would be available for decreasing the working bias by decreasing the MgO thickness.

Next, we studied the effects of applying an electric field repeatedly across the film while the current compliance limit was gradually increased. It has been observed in numerous resistive switching devices that memristor performance depends on the sweep frequency and maximum applied voltage/current limit imposed during the measurement [33]. As can be seen from Fig. 3(a), there are variations in the resistance values from one switching cycle to the next. The pulse with higher current leads to larger changes in resistance from the original state, which is a characteristic of multilevel-switch. However, it is a process poorly controlled in our case due to the limitations of the testing apparatus used for the setting of the compliance current, instead of, for instance, a transistor in parallel with a diode [27,33]. The new current–voltage

characteristic is essentially permanent provided the voltage does not exceed the forming voltage. The 10-fold curves shown in Fig. 3(b) illustrate the reproducible nature of the switching process.

4. Conclusions

In conclusion, magnetic devices exhibiting a bipolar resistance switching have been fabricated by sandwiching the MgO films between n-type conducting ITO and STO electrodes. The rectifying I–V characteristics of the device in its virgin state suggest the formation of a depletion p–n region at the top interface, as resulting from the introduction of oxygen vacancies within the ITO electrode during the annealing process. Holes near the p–n interface would diffuse into the n-type region. Thus, a single irreversible forming step at positive voltages prepares the sample for repeatable switching cycles. A possible mechanism for the resistance change is proposed based on the formation and rupture of conduction filaments due to the migration of structural defects in the electric field [4]. Following the fact that, if the hole density is sufficiently high the holes become delocalized [12,34], we propose this type of defect is most likely due to cation vacancies. Reference should also be made to the memristive switching measurements by Nagashima et al. [32] which highlight the role of redox events in oxides with p-type conduction paths. They propose hole carriers, and hence the device conductance, are compensated (enhanced) by a reduction (oxidation) process. In this regard, we had also demonstrated previously that cation vacancies are compensated by hydrogen reduction, thus controlling the reversible switching phenomena of ferromagnetism in these highly defective MgO films [18]. The agreement between these two types of experiments strengthens the interpretation of the ferromagnetic ordering in MgO on the basis of the Mg vacancies which polarize the O 2p-dominated upper valence bands.

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about $10^{10} \Omega\text{cm}$.

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Fig. 1. The I–V characteristics for STO/MgO/ITO n/p/n structure. (a) The initial measurements exhibit rectifying characteristics. The dc voltage was swept from negative to positive values and back, as shown by the arrows. Inset illustrates the device geometry. Following an electroforming step, (b) the device was switched on (showing an abrupt change in conductivity) by applying a positive bias. The current increase was limited by a compliance of 4 mA. Please note the change in vertical axes scale. Inset shows luminescence from a magnetic sample. The luminescence experiments were performed in dark, though the sample was partially illuminated during the photographic exposure.

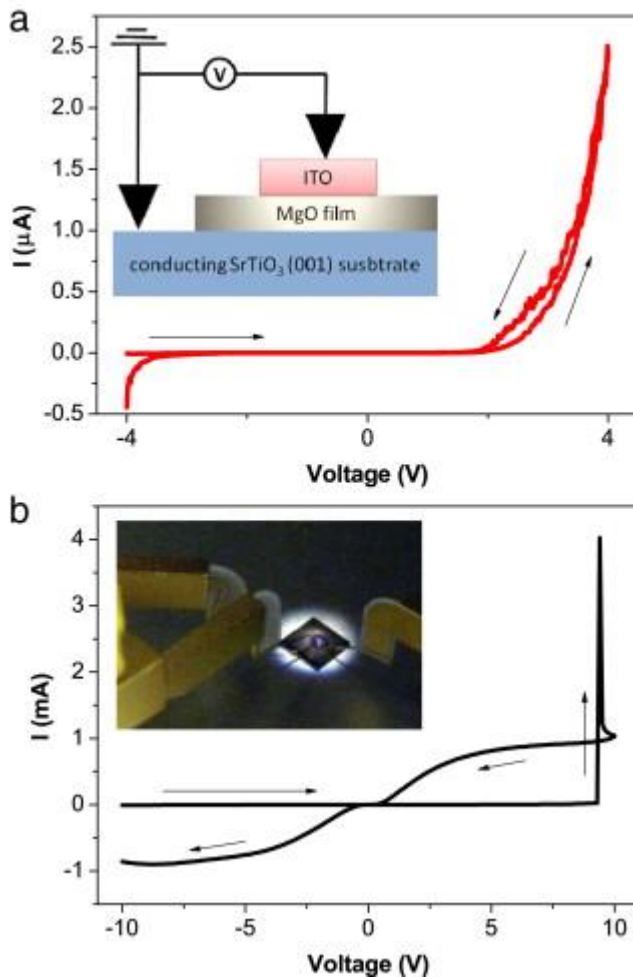


Fig. 2. Reversible bipolar switching I–V (a), and resistance loop (b) for the MgO-based device exhibiting two resistive states: a low resistance state, also referred to as the ON state and a high resistance state, referred to as the OFF state. ON/OFF conductance ratio remains about 100. Numbers 1–6 indicate the bias sweep direction.

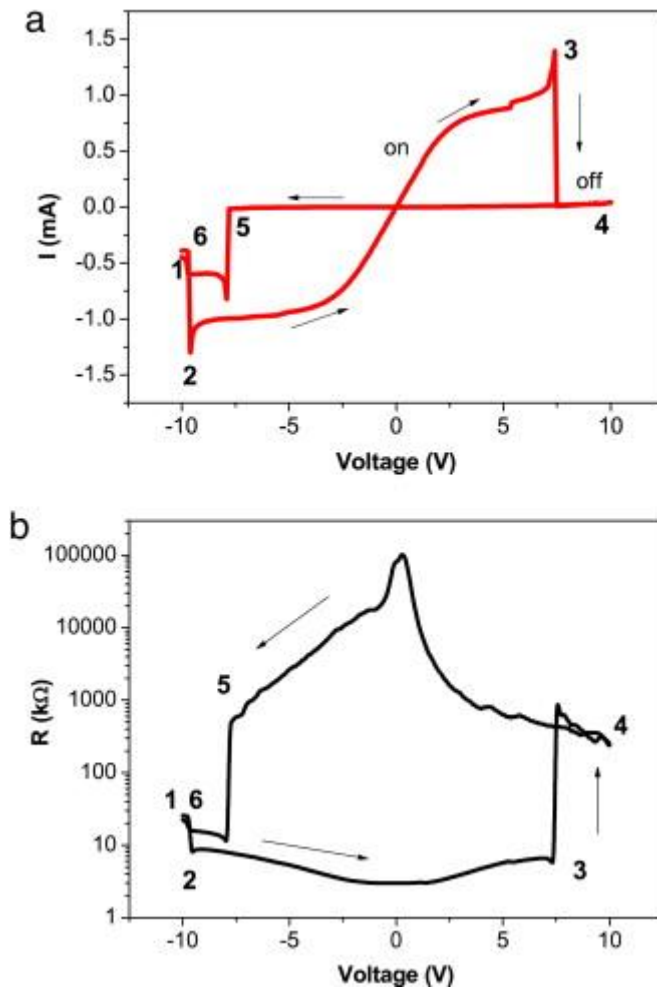


Fig. 3. (a) Multilevel resistance switching traces achieved by setting a compliance current of 4 mA (continuous heavy line) or 7 mA (open squares) during positive electroforming process. (b) Endurance test in the ± 10 V bias region: three consecutive sweeps are shown. Minor loop denotes the retention property of the high resistance (OFF) state at moderate voltages. No obvious degradation is observed after the 10th switching loop. Inset: typical semilog I–V characteristics.

