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A System-on-Chip Solution for a Low Power Active Capsule Endoscope with Therapeutic Capabilities for Clip Application in the Gastrointestinal Tract

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This paper addresses the circuit implementation challenges resulting from the integration of a therapeutic clip in a magnetically maneuverable wireless capsule intended for colonoscopy. To deal with the size constraints typical of a capsule endoscope, an Application Specific Integrated Circuit (ASIC) has been designed specifically to habilitate the release of the therapeutic clip. The ASIC is a complete System on Chip (SoC) that incorporates a circuit for the low power release of the clip, thus overcoming the limitations of the power supply system. With a size of 14 mm², the ASIC can be incorporated in practically any capsule endoscope, consuming only an idle-state power of 1.5 mW.

Keywords: Capsule endoscopy; therapy; endoscopy; low power; ASIC; BLDC micro-motor.



1. Introduction

Gastrointestinal (GI) endoscopy is a field that has progressed significantly in the last decades, both in terms of technology and clinical techniques and procedures. Being formerly used as a diagnostic tool, devices and techniques that allow first therapeutic interventions such as polypectomy were introduced. Today, endoscopists can perform procedures which before were exclusively covered by surgeons, such as hemostasis even in severe cases, resection of large areas of mucosa in a single piece, as well as closure of fistulas and perforations [1]. Novel devices and technologies continue to unfold the potential and importance of GI endoscopy for today's clinical use. The introduction of wireless capsule endoscopes around the beginning of the 21st century [2] allowed to reduce

the invasiveness of diagnostic procedures and inspect parts of the GI tract, in particular of the small bowel, that were difficult to reach with conventional flexible endoscopes before. This segment of GI endoscopy has gained importance in recent years. The technology has been proven robust and efficient.

Wireless capsule endoscopes present obvious advantages for the patient. This situation has stimulated the increase of the technical capabilities of such devices in order to extend their clinical application scenarios. At present, capsules cannot obtain biopsies, aspirate fluid or brush lesions for histology [3]. Nevertheless, when compared with flexible endoscopes, they reduce the discomfort and embarrassment of the patient, in principle allow access to the entire GI tract, reduce the length of hospital stay and procedural risk, and eliminate the need for sedation. Table 1 summarizes most of the capsule endoscopy advances done during the last years. Currently there are only a few commercial capsules [4–8]. At research level it is possible to find several capsules claiming outstanding performances in diagnosis [8–14]. There are also few devices focused on providing simple therapeutic capabilities [15–19].

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Table 1. Capsule endoscopes.

Module	Innovation Done	References
Diagnosis	CMOS image sensor	[6, 9–11]
	4 image sensors	[8]
	HD CCD sensor	[7]
	PH and Temp. sensors	[11, 23]
Therapy	Fluorescence diagnosis	[13, 14]
	Drug delivery	[5, 18]
	Biopsy	[17, 19]
Powering	Clipping	[16], this work
	Wireless power transfer	[24, 25], this work
	Control and processing	ASIC
Micro-controller		[11]
FPGA		[22, 26]
Comms	Image compression	[10, 22, 26]
	Wireless (Mid-ISM)	[6, 7, 9–11, 22]
Movement	UWB	[27]
	Passive	[6–11]
	Magnetic	[19], this work
	Legged	[28]
	Worm-like	[29]
	Propellers	[30]

The aim of this work is to overcome some of the current limitations of capsule endoscopy by incorporating a new endoscopic over-the-scope (OTSC) system [20, 21] in a capsule-like device to habilitate clipping in the colon. Given the large caliber of the colon lumen, the capsule has to be stabilized externally. A capsule with the standard elements has been adapted to carry a permanent magnet. Locomotion is controlled by an extra-corporeal permanent magnet manipulated by a robotic arm.

A recently developed clipping capsule endoscope using commercial off-the-shelf electronics [22] is used as a base technology [16, 23]. The clip release is performed using a brushless direct current (BLDC) micro-motor. The major contribution of the presented work is to integrate BLDC motor drivers and the control electronics in an Application Specific Integrated Circuit (ASIC), which highly reduces the space required by the electronics as well as the power consumption. The ASIC is a System-on-Chip that integrates a microcontroller, motor drivers, I2C controller and a low speed serial communication unit to receive commands.

In this paper, the authors describe in Sec. 2 the capsule-like device. The architecture of the System-on-Chip and its main features is described in Sec. 3. In Sec. 4, the low-power BLDC motor driver is discussed in more detail. System integration is explained in Sec. 5.

2. Therapeutic Device

Most of the elements of the therapeutic device are common in conventional capsules. For diagnosis in the

small intestine a camera, a transceiver, LEDs, optics, a dome and a shell are used. In conventional capsule endoscopy the images are sent wirelessly by the transceiver integrated while the capsule moves by peristaltic movements. In the case of exploration of the colon, active control of the capsule position and orientation is required. In the clip deployment this is also necessary. The therapeutic device will be moved actively through the colon to the region of interest and will be used to release the OTSC clip in a secure and controlled way. The motion of the therapeutic capsule is achieved by means of an external magnetic locomotion control [23–25]. Movement is obtained by the interaction of a magnet guided by a robotic arm above the patient and a permanent magnet inside the capsule [26, 27]. To continuously track the position and orientation of the device is possible using an inertial sensor and a special arrangement of the magnet inside the capsule as previously done in [28]. The inertial sensor data allows for a closed sensor feedback cycle that stabilizes the endoscope to perform a soft and secure movement.

The OTSC clip is placed in an applicator cap mounted on the capsule tip. Figure 1 shows an image of a therapeutic capsule endoscope that integrates the mentioned clip. The total size of the capsule-like device with the clip is 37 mm × 13 mm, which can be used easily in the colon. The OTSC is a device made of super-elastic Nitinol, which is biocompatible for long-term implantation. In the clinical field it is used in hemostasis [29, 30], closure of fistulas [31] and perforations [32]. The clip is shown in

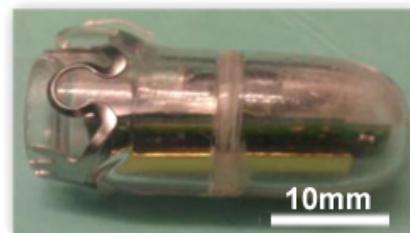


Fig. 1. Capsule-like therapeutic endoscope. The OTSC clip is at the tip of the capsule.



Fig. 2. OTSC clip.

Fig. 2. Based on its unique design, the clip closes itself and firmly anchors the tissue to be compressed for hemorrhage or closure of a GI organ wall lesion. The clip is released just once. A Faulhaber BLDC motor (0515B motor with 06A gear and a reduction ratio of 625:1) is used to pull a biocompatible wire attached to the clip. BLDC motors can provide enough force to deploy the clip (c.a. 10 N). A schema of the releasing concept is shown in Fig. 3. When the command to release the clip is received, the BLDC motor is continuously pulling the wire and winding it onto the shaft until the clip is released. The operation speed is limited by the available power. Pulling until the release of the clip avoids an unintentional release in case of power failure.

It is not possible to do the driving of a motor with the chips currently used in capsule endoscopy. Moreover, the utilization of off-the-shelf components for driving the motor is difficult because of the lack of free volume after the incorporation of the motor in the capsule. Apart from that, the therapeutic device is also constrained by the limited power. A high current is necessary to release the OTSC clip, in particular during the start-up of the BLDC motor [33]. This severely limits the integration of therapeutic units in the size of an endoscopic pill. To overcome this obstacle, an ASIC that integrates a new BLDC motor driver, designed to reduce the power consumption during the start-up of the motor, has been used. The custom Integrated Circuit (IC) also solves the problem of the reduced volume available, as it can substitute several off-the-shelf chips and passive components that would be needed to manage all the capsule functions and the release of the clip. The IC acts as a slave device, driving the BLDC motor when the activation command is received telemetrically. The ASIC is programmed from a small EEPROM memory during the power-up. In this development, an off-the-shelf microcontroller controls the inertial sensor dataflow, the ASIC and the transceiver. Orders from the external operator are also managed by the microcontroller. The architecture of the endoscope including these elements is schematically shown in Fig. 4. The capsule is powered with a 3D inductive link that can provide up to 250 mW in the operation conditions. Details of the 3D inductive link can be found in [26, 34].

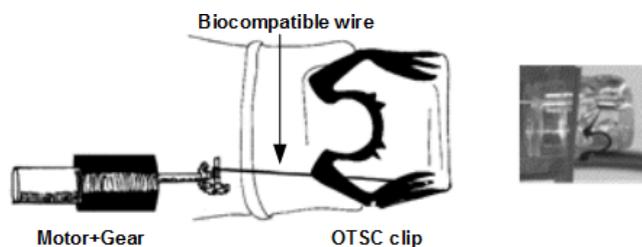


Fig. 3. Schema and detail of the release mechanism. The BLDC motor pulls a wire attached to the OTSC clip.

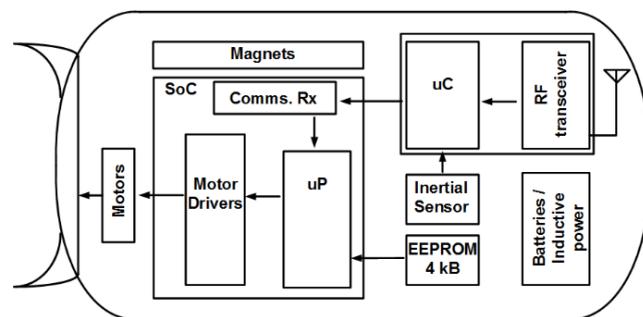


Fig. 4. Schematic of the architecture of the therapeutic capsule.

3. System-on-Chip Architecture

The ASIC has been fabricated in a 0.35 μm , high voltage technology because it offers enough power capabilities to drive the motor as well as the high density required for the integration of the digital controller and satellite functions. The ASIC can be used either in master mode to control the basic functions of a capsule plus the driving of the motor or in slave mode managed by the microcontroller in the therapeutic device.

The architecture of the ASIC is illustrated in Fig. 5. The different modules of the ASIC are indicated in the ASIC photograph of Fig. 6. An embedded 8051 microprocessor acts as the control unit (Synopsys © DW8051 [35]) of the IC. The inclusion of the microprocessor in the ASIC provides more flexibility to the system and facilitates the debug. The microprocessor has 256 B of internal SRAM memory. The ASIC uses 512 B of SRAM data memory and 4 kB of SRAM program memory. As the memory used is volatile, each time that the ASIC is powered up, the program has to be uploaded to the memory. Since EEPROM memories have not been integrated in the ASIC, an external 4 kB SPI EEPROM memory is used to program the IC.

To achieve the necessary low-power, a Power Management Unit (PMU) controls the clock of each block. Among other activities, the PMU is responsible for starting the system with low-power consumption. This is necessary in this autonomous device. During power-up, the Power-on-Reset signal forces the PMU to activate the Boot Loader (BL) only, which reads the external EEPROM and programs the internal memory. This process is carried out at 10 kHz in order to have low power consumption. Once programmed, if the ASIC is in master mode, it wakes up the microprocessor. When it acts as a slave, it waits for orders via the I2C bus or through the communication module (CCU). The other blocks rest in power down mode until the PMU receives the order to wake them up.

The I2C control unit (I2CCU) is a finite state machine (FSM), which can work as a master or as a slave. External resistors have been used in open drain outputs to have I/Os

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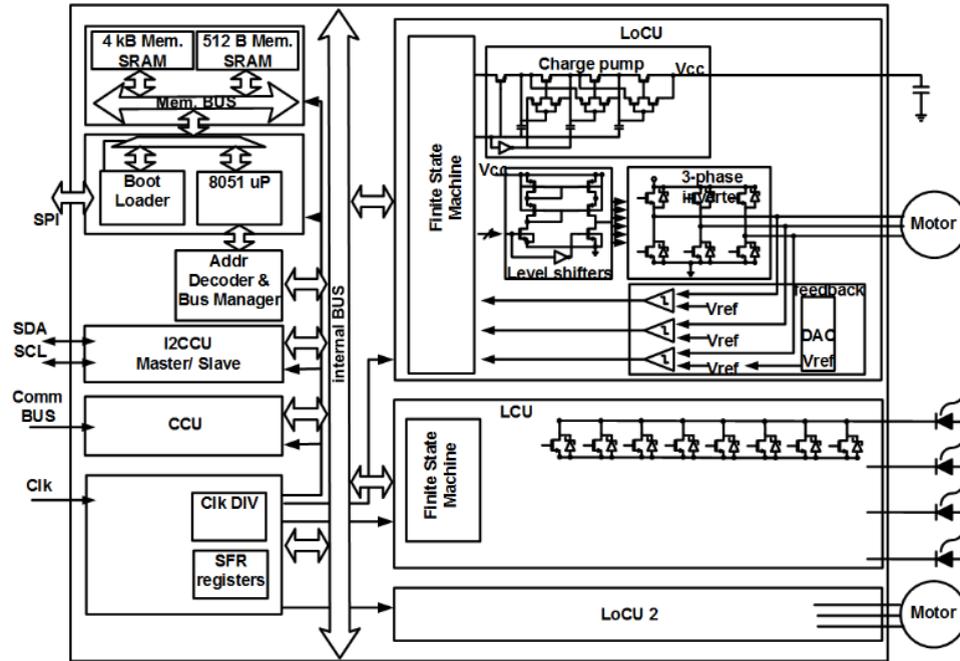


Fig. 5. Schematic of the architecture of the mixed-mode low-power IC.

with pull up resistors of 1 kOhm, which are necessary to assure the correct operation of the I2C-like bus. This module allows to integrate other I2C compliant devices to the capsule. For example, it is possible to enable vision in the presented capsule endoscope by integrating a camera module and a dedicated transceiver that directly sends the

images to the external receiver; both controlled by the I2C control unit.

Data from the off-the-shelf microcontroller included in the therapeutic capsule is received by the CCU. The CCU is a FSM that decodes and feeds the received data to the embedded microprocessor. The data is received at 4.8 kbps by a serial input. The CCU usually is in sleep mode. To start the receiving process, the transmitter has to send a logic one followed by the data (16 bits). Once received, the CCU generates an interruption and returns to sleep mode. The interruption is processed by the embedded microprocessor. The ASIC also includes four LED drivers (LCU) that are used to control the illumination during the navigation of the capsule. Two motor drivers (LoCU) have been included. It is possible to control up to 3 DC motors or 2 BLDC motors at the same time. Nevertheless, only one driver is necessary for each OTSC clip. The BLDC motor driver is based on a traditional 3-phase inverter structure, which is typically used for BLDC control [36–38]. Table 2 summarizes the main characteristics of the ASIC.

4. Low-Power BLDC Micro-Motor Driver

BLDC motors have high efficiency and reliability, are low noise, present a long lifetime and generate low electromagnetic interferences. BLDC sensor-less micro-motors are widely used in challenging areas of application such as medical devices, handling automation and precision optics. In small autonomous devices the feasibility to use

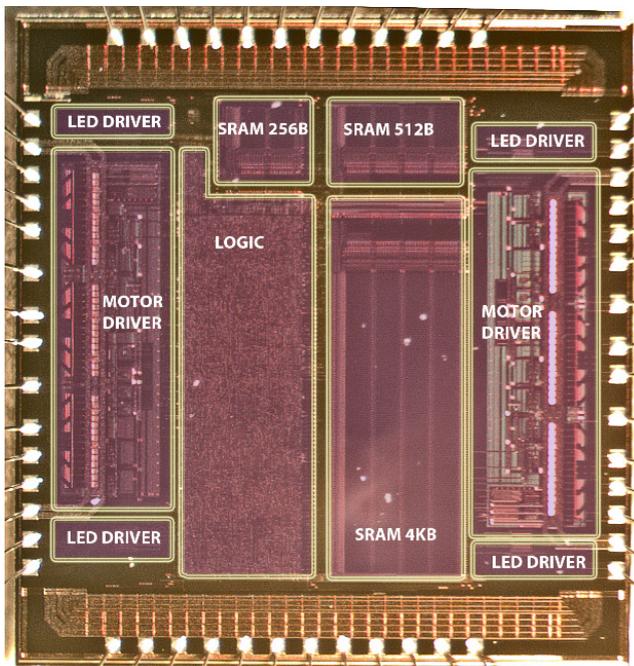


Fig. 6. Schematic of the architecture of the mixed-mode low-power IC.

Table 2. Summary of the main characteristics of the ASIC.

Characteristics	Property	External Devices Needed
Size	3.65 mm × 3.85 mm	
Power (IDLE)	1.5 mW	
Thickness	250 um	
N° equivalent gates	20 k	
Memories	1 SRAM (4 kB)	
	1 SRAM (512 B)	
	1 SRAM (256 B)	
Analog circuits	2 DAC (8-bits)	
	6 Comparators	
	2 3-phase inverters	
	2 Charge pumps	
	1 Power on reset	
Modules	6 Level-shifters	
	LED drivers	16 LEDs
	BLDC driver	36 nF BLDC motor
	Master/Slave I2C	2 pull up resistor (1 kOhm)
	Communication unit	

BLDC micro-motors is conditioned by the capacity of the supplying system and the reduced available space. Nowadays, most of the solutions that integrate BLDC micro-motors rely on the reduction of the power consumption of the driver or the supply of recharges for the battery. Although these solutions extend the battery lifetime, they do not have any effects on the start-up of the motor. In a conventional start-up, the current required may be higher than that delivered by the power system. As a consequence, the electronics may fail due to the excessive current delivery and the drop of the biasing voltage. Even in the clipping application, where the clip is released only once, this limits the applicability of this type of motors. Apart from that, BLDC motor drivers must deal with the problem of the freewheeling currents, thus requiring volume to include external freewheeling diodes or implement synchronous rectification. In this work, the conventional motor driver has been modified in order to perform a low power start-up while reducing substantially freewheeling currents without using external freewheeling diodes or synchronous rectification. This methodology eliminates the need for external elements and hence reduces the volume required for the control of the motor. A driver for BLDC motors has been designed to eliminate the two above mentioned problems.

The control of the motor is usually done with a three-phase inverter in a six-step commutation schema [39], where commutation is controlled by position sensors. To reduce the cost and complexity of the drive system, a sensor-less drive is preferred [40–43]. In sensor-less operation, the commutation is based on the measure of the back electromotive force (BEMF). The start-up of the

motor is critical and sometimes difficult for a BLDC sensor-less system. Once the rotor is aligned, it is possible to measure the BEMF signal and perform the sensor-less control with the integrated FSM by means of Pulse Width Modulation (PWM). This control allows to regulate the power according to the motor operation [44]. The magnitude of the back-EMF is directly proportional to the motor speed. This makes it extremely difficult to measure the BEMF at low speed, since the signal-to-noise ratio is very small. To accelerate the motor, a start-up process is applied to the BLDC motor, as it is shown in Fig. 7. A practical start-up tuning procedure consists in cycling the bridge at maximum speed until the wings are tuned in a few steps (typically in 6–12 steps). During this stage, the transistors in the bridge are opened for more time than in the stationary phase to assure the movement of the rotor (see the alignment phase in Fig. 7). As a consequence of the higher torque needed during the start-up, a higher current goes from the bias to ground through the bridge transistors. The BEMF feedback stage is composed of three comparators and an R-2R digital to analog converter. Each comparator senses each motor phase. Proper BEMF reading can be achieved by skipping one electrical commutation ahead by 120° and waiting for the zero crossing on the respective floating coil.

The Faulhaber micro-motors used in the wireless device require up to 100 mA during the start-up, which is unaffordable with the available powering system (250 mW @3.3 V are delivered by the RF inductive link). This justifies the utilization of a driver specifically designed to activate the therapeutic clip. The schematic of the driver was presented in Fig. 5. The three-phase inverter structure was designed with 5 V transistors and is supplied at 3.3 V. The current that goes through each phase of the bridge is given by the MOSFET saturation current:

$$I_{Dsat} = \frac{\mu_n C_{OX} W_n}{2L_n} (V_{GSn} - V_{THn})^2, \quad (1)$$

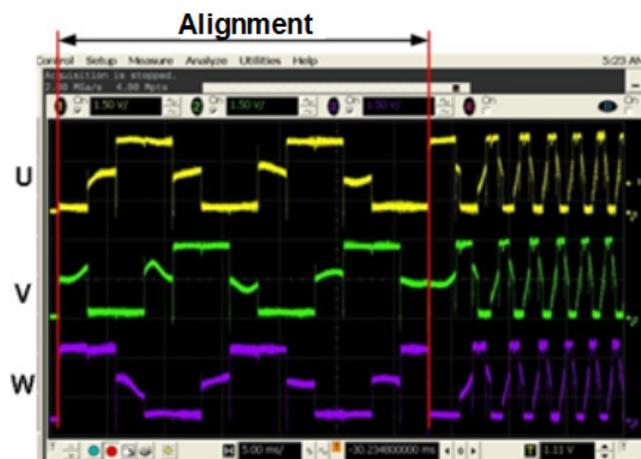


Fig. 7. Start-up of the BLDC motor using 12 steps to align the rotor. U, V and W are the 3-phase bridge control signals.

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where μ_n is the charge-carrier effective mobility, C_{ox} the gate oxide capacitance per unit area, W_n the gate width, L_n the gate length, V_{GS} the gate-source voltage and V_{TH} the threshold voltage of the n MOS transistor. From (1), two methods to limit the current supplied to the motor can be inferred. It is possible to control either the supply voltage or the gate voltage of the transistors used in the driver. Previous works have been focused on the control of the supply voltage [45, 46]. The approach followed here is based on the control of the gate-source voltage during the start-up. The advantage is that it is easily integrable in the driver without increasing the driver area, which is important in capsule endoscopy. Figure 8 shows the current flowing through one of the phases of the bridge for different transistor sizes and gate-source voltages. The phases were designed with n MOS transistors only, which are isolated in different wells to avoid latchup issues and noise affecting the rest of the ASIC. The transistor size is fixed in the ASIC by the maximum necessary current, while the driving current used, is controlled by the gate-source voltage. A Dickson type charge pump [47] and 6 level-shifters drive the transistors in the three-phase structure (Fig. 5). The charge pump can generate up to 5 V from a 1.8 V source. The output voltage of the charge pump is controlled by changing the operation frequency and the PWM signal, as usual. The level-shifters are used to adapt the controlling signals from 3.3 V to the new levels generated by the charge pump. During the start-up, the charge pump is programmed to the minimum output voltage (2.5 V). This voltage is fixed by the PWM signal and the input of the charge pump ($V_{bias} = 1.8$ V). In the stationary phase, the PWM signal is adapted to increase the output voltage up to 5 V and the motor speed is handled by the FSM motor controller.

A comparison between the current consumption during a normal start-up and the low power start-up is

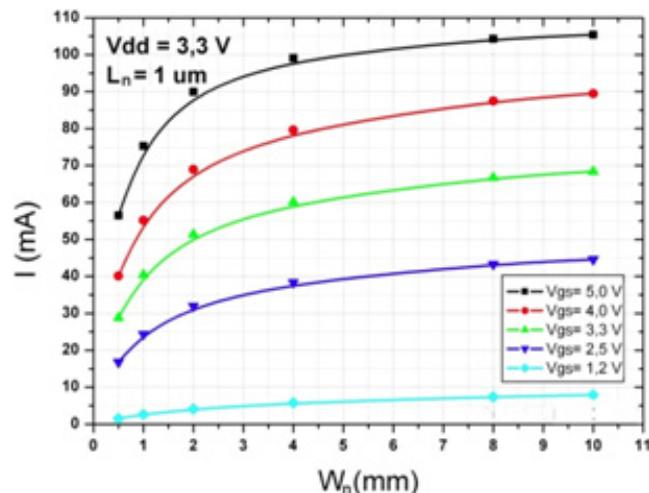


Fig. 8. Current obtained for the n MOS/ n MOS structure of the bridge depending on the width, W_n , of the transistors and the voltage at the gate of a n mos transistor, V_{gs} .

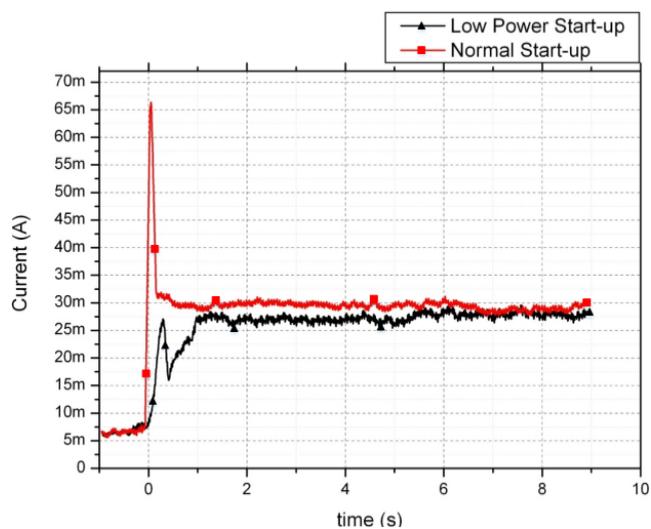


Fig. 9. Comparison between the current needed by a Namiki BLDC motor during a normal start-up (red) and a low power start-up (black) versus time.

illustrated in Fig. 9 for a Namiki BLDC motor. To demonstrate the low power operation mode of the driver, the charge pump has been programmed to generate a ramp from 2.5 V to 5 V in 1 s. 1 s is approximately the minimum time required to have the rotor aligned with the controlling signals. The result has been compared to a normal start-up where the charge pump directly generates 5 V. As shown in Fig. 9, the current peak of the start-up is significantly reduced from 66 mA to 27 mA by controlling the gate-source voltage during the start-up. The same procedure can be applied to other motors. The final prototype of the capsule has a Faulhaber motor. The main difference when compared with the Namiki motor is that it requires 100 mA during a conventional start-up. Nevertheless, the torque is higher.

The driver also deals efficiently in terms of space with the freewheeling current problem of BLDC drivers. Instead of using external freewheeling diodes or synchronous rectification, the control of the switching time of the transistors in the driver has been performed to reduce freewheeling currents. The transistors of the three-phase structure are switched on/off slowly enough to reduce the effect of the magnetic flux on the driver. As shown in Fig. 10, a switching time of 30 μ s almost eliminates the freewheeling current. It also induces deformed spikes in the current, but these do not alter the motor operation. In the presented driver, the slow switching time has been implemented with the proper transistor sizing of the level-shifters.

5. System Integration

The therapeutic device has been fully integrated to prove that a clipping mechanism can be done in a wireless

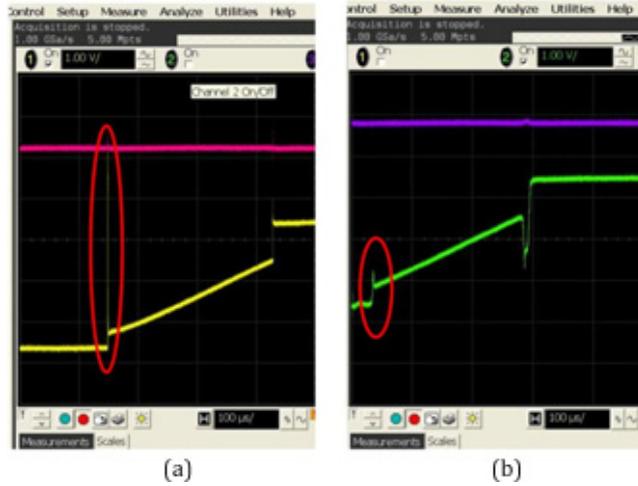


Fig. 10. Oscilloscope images of the current in a motor phase (bottom lines). (a) Driver with freewheeling currents. (b) Driver with control of the switching time and without freewheeling currents.

capsule endoscope. The therapeutic endoscope consists of a dedicated printed circuit board (PCB), a Faulhaber BLDC micro motor, the OTSC clip, a magnet used for magnetic locomotion and a 3D inductive link, all inside the endoscope shell (Fig. 1). The PCB has some accessible pins that are used to program the EEPROM memory and the microcontroller.

All the electronics needed are assembled in a single PCB (Fig. 11). It includes a CC1101 receiver, a DSC1001 oscillator and an EEPROM memory on the bottom side. The microcontroller, a level-shifter (used to adapt the 1.8V signals from the microcontroller to 3.3V for the ASIC), the inertial sensor, the ASIC and two LDOs used to generate 1.8V and 3.3V are on the top side. The size of the PCB is 33 mm × 12 mm, and therefore fits in the capsule. Table 3 summarizes the list of components and their power consumption. Nevertheless, it is clear that

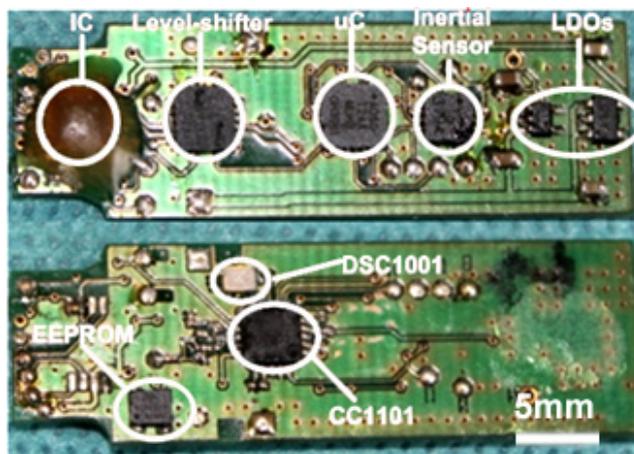


Fig. 11. Photograph of the PCB integrated in the capsule.

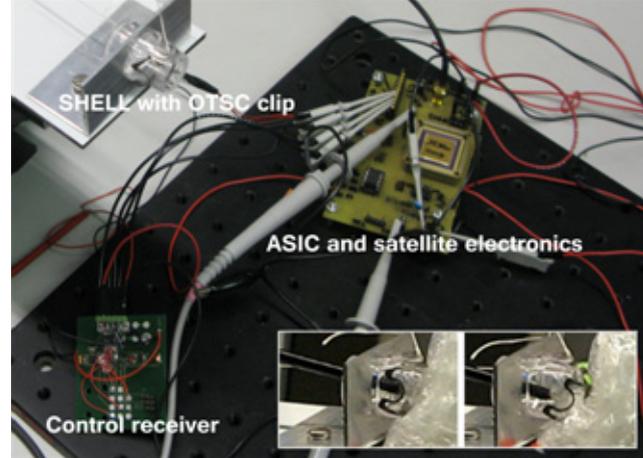


Fig. 12. Setup for the test of the clip release and release sequence.

considerable further miniaturization can be achieved by integrating most of these elements in the ASIC.

During the test, a 3D inductive link was used to power the device [34]. Although this 3D inductive link is capable to generate up to 400 mW, in the present application it can only supply 250 mW since the internal magnet of the capsule modifies the magnetic field. Interferences between the inductive link and the electronics have not been observed as they work with a significantly different frequency [48].

Different tests were conducted in order to fix the conditions under which the clip is released to perform the desired action. The release of the clip is possible only if a low power start-up is used. The maximum current possible is limited to 65 mA. With a conventional start-up it is not possible to deploy the clip because the Faulhaber BLDC motor requires nearly 100 mA. Figure 12 shows the setup used to test the release of the clip.

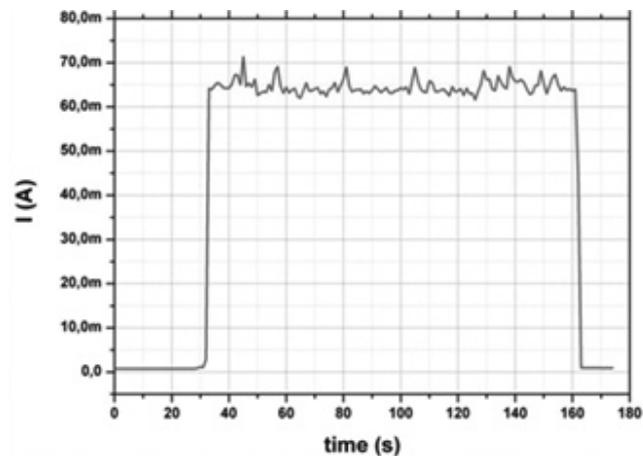


Fig. 13. Measured current in the motor during the release of the clip.

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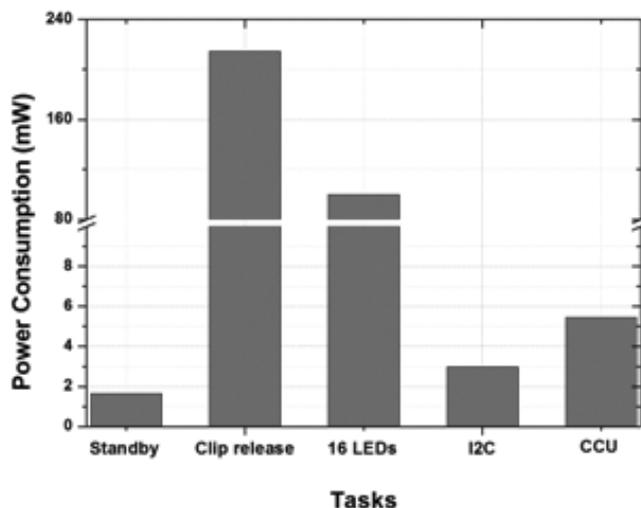


Fig. 14. Measured power consumption of the ASIC.

The release order is sent wirelessly by an external unit. A control receiver is connected to the ASIC, which is assembled in a test PCB with satellite electronics. The ASIC is powered by the output of the RF inductive link system (not shown). The ASIC drives a Faulhaber motor pulling a biocompatible wire connected to the clip, which is on a capsule shell. Figure 13 shows the current delivered to the motor during the releasing of the clip. The release process takes approximately 130 s. In Fig. 14, the power required by the different modules of the ASIC is plotted. The stand-by power is 1.5 mW. This low power is achieved by controlling the activity of the digital part and switching-off most of the analog modules. The driving of the motor consumes a maximum power of 215 mW during the release of the clip, which is performed only once. The power consumption of the digital modules always includes the power of the embedded processor. The clock of the system is 10 MHz. To limit the power consumption, dynamic frequency scaling (DFS) is used. With DFS, the total power required for operation of the digital part during the organization of the different tasks (clip release, LED activation, communications) is always below 10 mW. This power management strategy allows

Table 3. Summary of the components included in the capsule device.

Module	Part Name	Power Consumption
EEPROM	25LC320	0.5 mA (read) @ 1.8 V 0.5 μ A (standby) @ 1.8 V
uC	Atmel ATtiny24	0.3 μ A @ 1.8 V
Oscillator	DSC1001	7.2 mA @ 1.8 V
Comms.	CC1101	15.6 mA @ 1.8 V
Inertial sensor	LIS3DH	11 μ A @ 1.8 V
ASIC		1.5 mA (IDLE) @ 3.3 V 215 mA (MOTOR) @ 3.3 V

to never exceed the total available power of 250 mW of the RF inductive link.

6. Conclusions

Since the apparition of the first capsule endoscope in the market, most of the developments in capsule endoscopy have been focused on improving the conventional capabilities of the state-of-the-art capsules, especially the vision, in order to extend the clinical applications of wireless capsule endoscopy. One important aspect that can be considered as a current drawback of wireless capsule endoscopy is that capsules cannot be actively maneuvered to perform any therapeutic intervention. A step forward towards capsules which are equivalent to flexible endoscopes has been done. With the prototyped ASIC, the BLDC micro-motor and the OTSC clip it is possible to approach to a GI lesion in a controlled manner to perform a therapeutic intervention. The ASIC is a complete controller of the therapeutic clip. It integrates a low-power BLDC motor driver to allow activation of the clip with current technology for micro-motors and powering at the capsule size scale. The OTSC clip, the BLDC motor, the control ASIC and the inductive power system plus some magnets needed to enable active locomotion, have been added to a capsular shape of only 37 mm \times 13 mm. Thanks to the integration achieved the final size of this endoscope perfectly fits to the operation in the colon.

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Oscar Alonso is Doctor in Engineering and Advanced Technologies (27th April 2012) by the University of Barcelona. His Ph.D. Thesis, entitled ‘Enabling active locomotion and advanced features in an endoscopic capsule’ and carried out under the supervision of Dr. Angel Diéguez, was qualified cum laude and was awarded with the Honors Doctorate by the Univesity of Barcelona.

Physicist and Electronic Engineer, with nine years experience in CMOS design, FPGA operation and system integration. Expert in desing of Application Specific CMOS Integrated Circuits. In the last five years, his research has been focused in capsular endoscopy and high-sensitivity photon detectors, mainly based on Avalanche Photodiode Devices, applied to high energy particles detection, molecular fluorecence signals and *in vivo* imaging. He is co-founder of ENDOASIC Technolgy, a start-up company of the department of electronics focused on integrated imaging in endoscopic environments.



Angel Diéguez received the Ph.D. degree in Physics from the University of Barcelona (UB), Spain, 1999. Since 2001, he is an Associate Professor at the Department of Electronics of UB. His research activities have been developed mainly in the field of basic semiconductor research and microelectronic circuit design. He has participated actively in six European projects and has more than 40 papers. His main areas of interest are mixed-signal design of ICs and low-power VLSI design. Specific research

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Sebastian Schostek graduated in Engineering Physics from the Munich University of Applied Sciences in 2004, and received his Ph.D. degree from the Faculty of Medicine of Eberhard Karls University Tuebingen in 2010. He worked as research fellow and Lecturer at the IHCI - Institute of Healthcare Industries, Steinbeis University Berlin, before he joined novineon Healthcare Technology Partners GmbH, Tuebingen, as Business Unit Director in the fields of technological and medical research, medical product development, contract research, and business consulting. Throughout his career, he was involved in a number of national and international research projects. His work led to numerous publications, patents, lectures and awards. In 2011, he accepted the position as Vice President of the Division Diagnostic Systems at Ovesco.



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